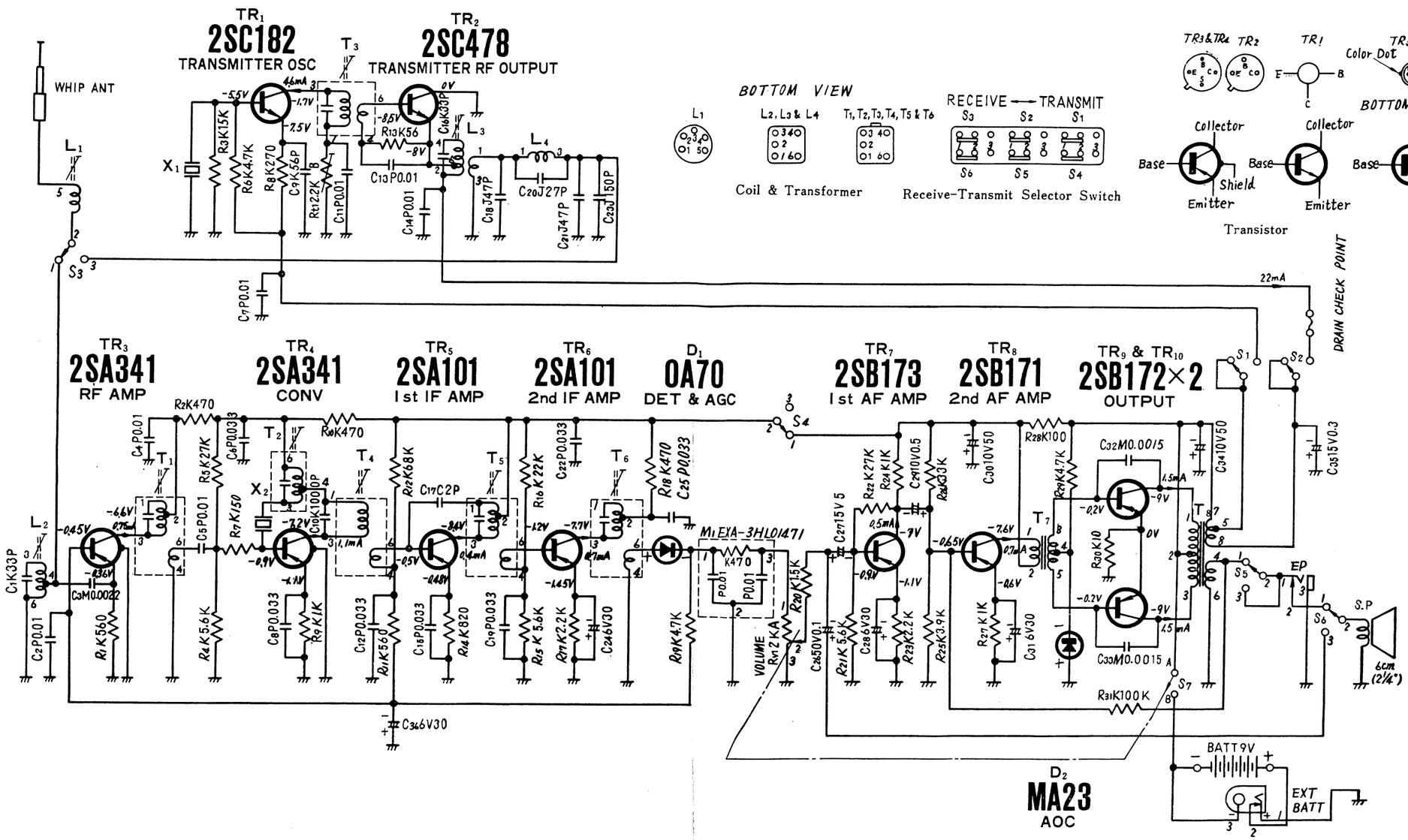
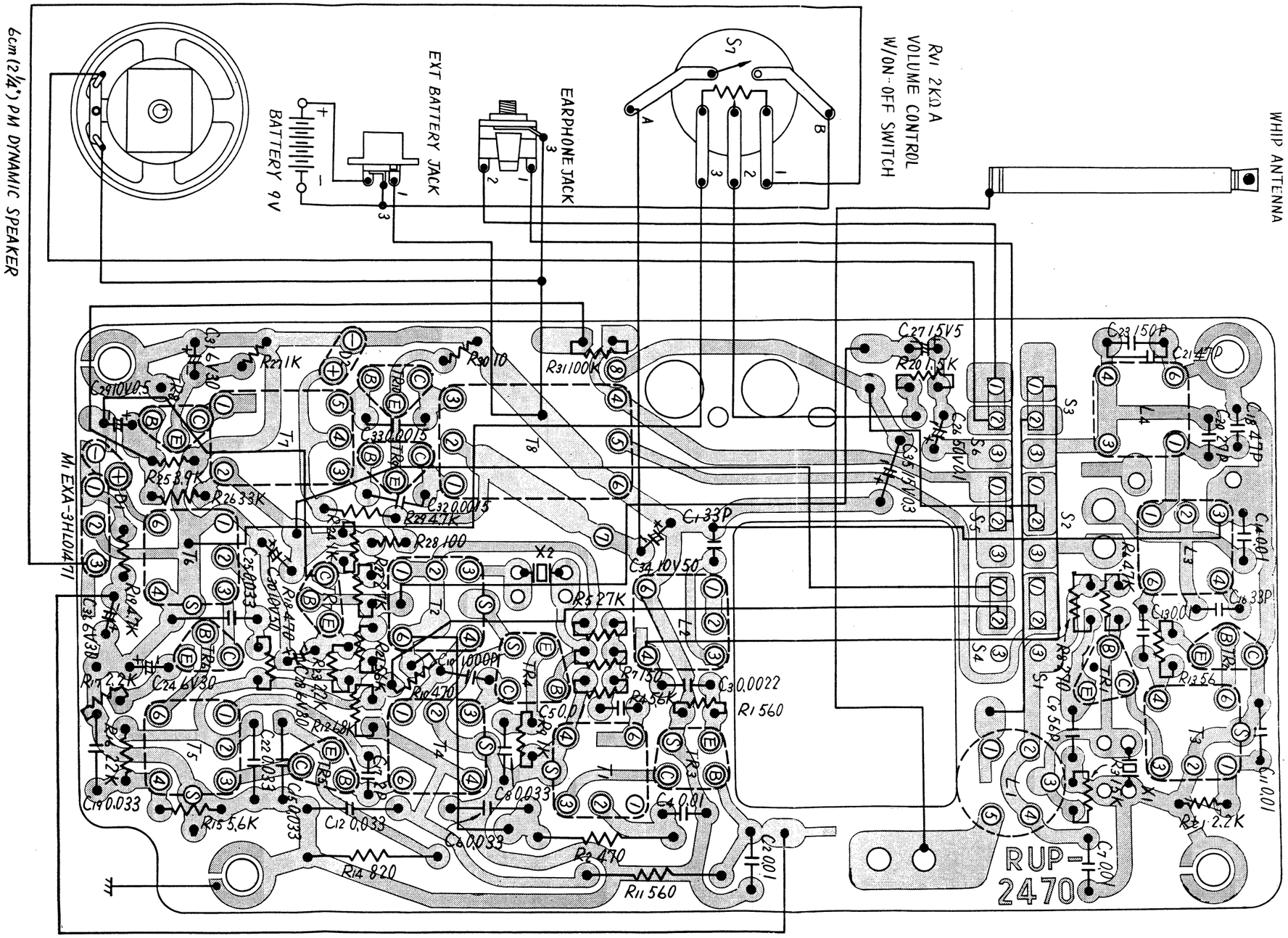
MODEL RJ-11E



Notes:

1. When replacement of a transistor is necessary, realignment may be necessary.
2. S1~S6: Receive-transmit selector switch in "RECEIVE" position.
3. S7 : Power source switch in "OFF" position.
4. Voltages indicated are measured from positive side of battery terminal with a 10KΩ/volt voltmeter under no signal condition.
5. Capital letters (C, J, K, M, P) in the circuit diagram show allowable tolerances of resistors and capacitors as follows:
   C=±0.25PF J=±5% K=±10% M=±20% P=+100% — 0%
6. Battery current: Receiver.....10mA (No signal)
   .....72mA (Maximum output)
   Transmitter.....27mA
7. PF=pico farad=mmf
   μF=micro farad=MF

Schematic Diagram.



1. All resistor values in ohms (K=1000Ω).
2. All capacitor values in micro farads (P=μμF).
3. S1~S6 : Receive-transmit selector switch in "RECEIVE" position.
4. S7 : Power source switch in "OFF" position.

Circuid Board Wiring View (Conductor Side).